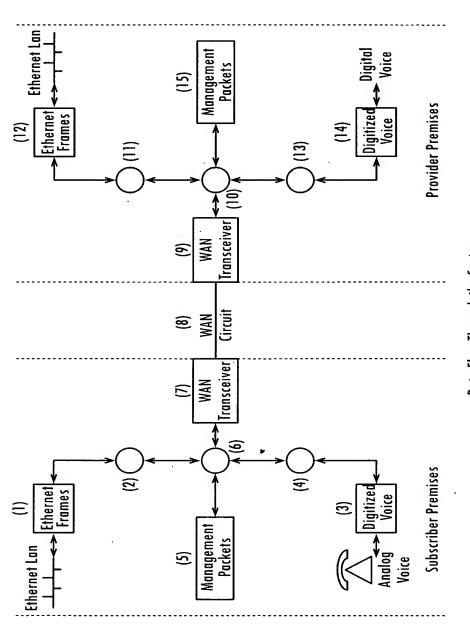
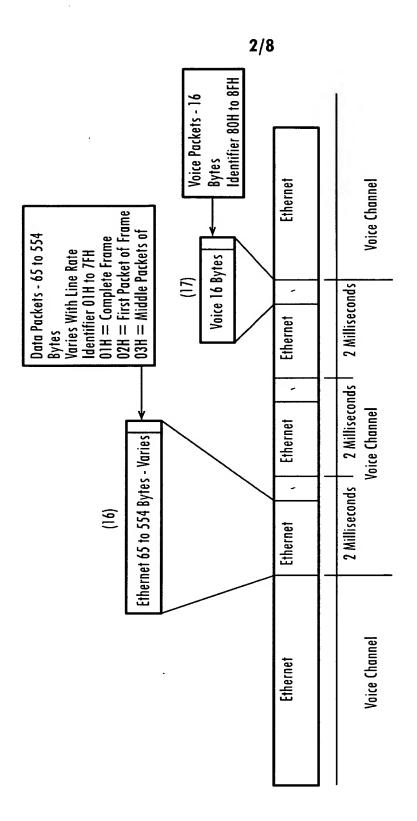


1/8

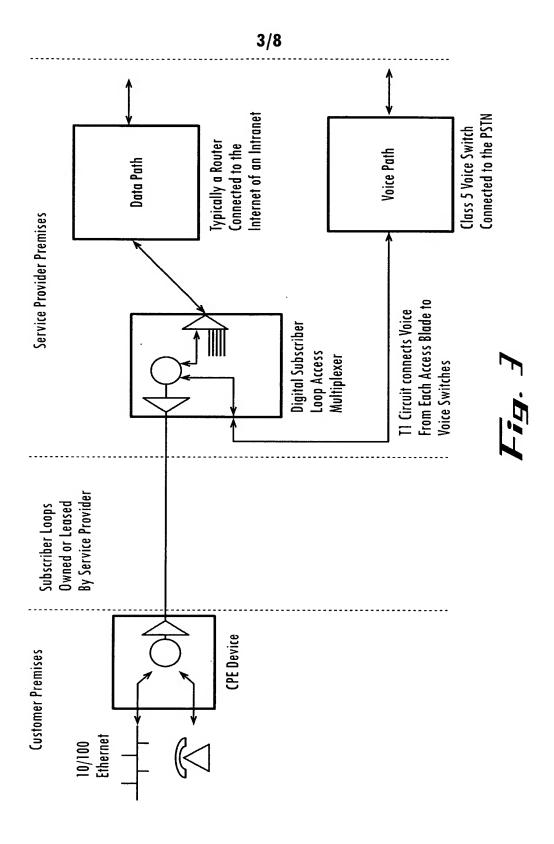


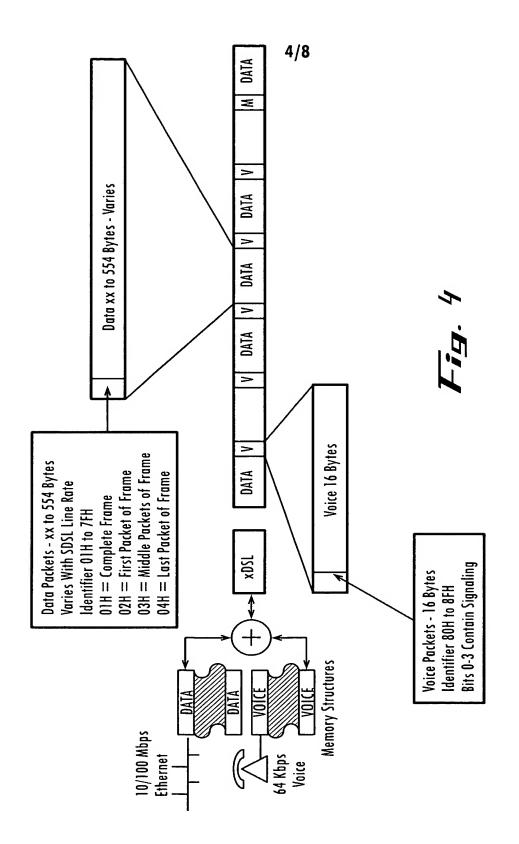
Data Flow Through the System



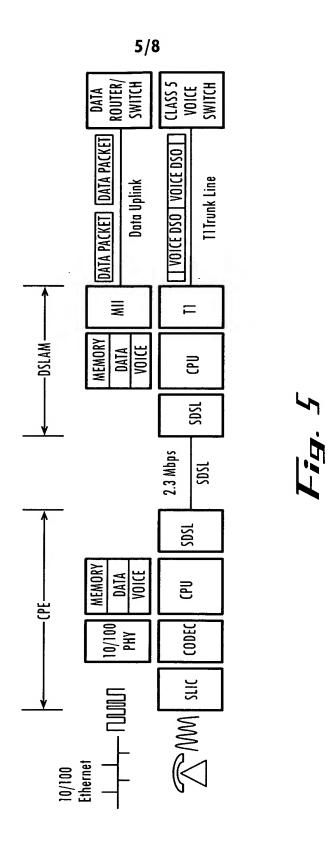
Packet Structure on the WAN Circuit



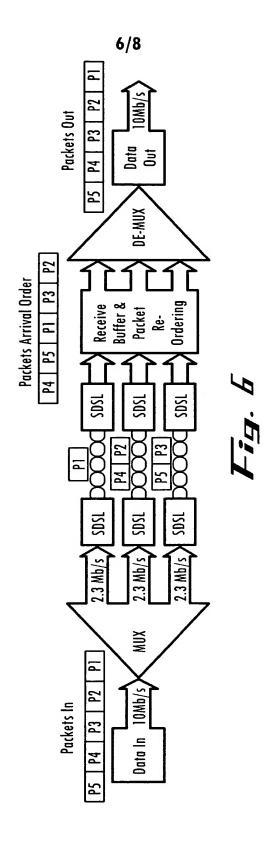




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